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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2827  
Examiner: Luan C. THAI  
Confirmation No. 9071

In Re PATENT APPLICATION Of:

Applicant(s): Kazutaka SHIBATA

Serial No.: 10/091,544 ✓

Filed: March 7, 2002

For: SEMICONDUCTOR DEVICE HAVING A  
WARP PREVENTING SHEET

Docket No.: AI 271

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)  
)  
) **SUBMISSION OF**  
) **FORMAL DRAWINGS**  
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December 2, 2003

**BOX DRAWING REVIEW BRANCH**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowance dated November 17, 2003, Applicant hereby submits one (1) sheet of corrected Formal Drawings (Fig. 1). The formal drawings incorporate the Examiner approved proposed drawing changes submitted on October 15, 2003.

Respectfully submitted,

Robert H. Berdo, Jr.  
Registration No. 38,075  
RABIN & BERDO, PC  
Customer No. 23995  
Telephone: 202-371-8976  
Facsimile: 202-408-0924

December 2, 2003  
Date

**FEE ENCLOSED: \$ 0**  
**Please charge any further**  
**fee to our Deposit Account**  
**No. 18-0002**

RHB:crh